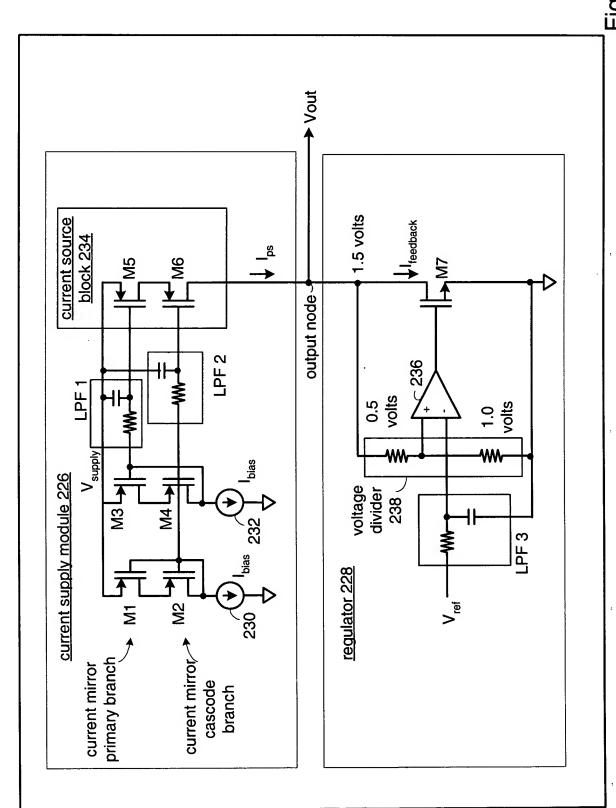


Figure 5 MGT FPGA 200



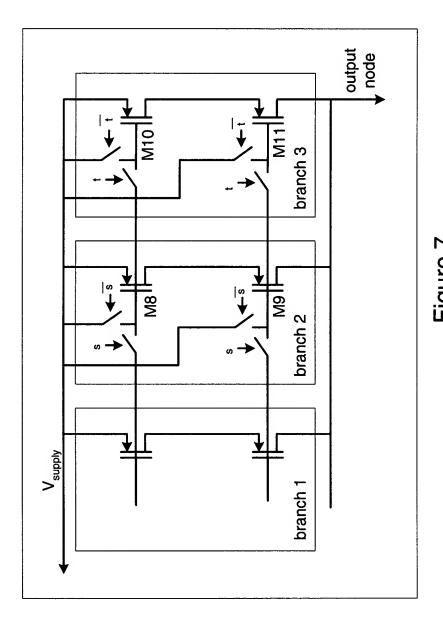


Figure 7 current source block 234

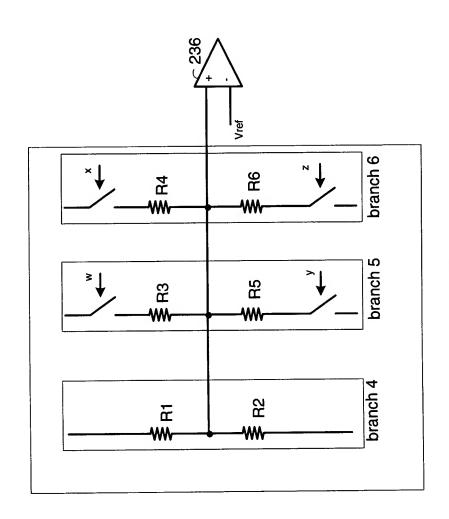


Figure 8 voltage divider 238

